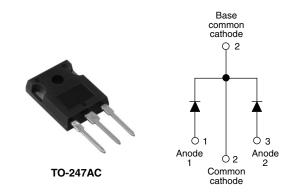
RoHS

COMPLIANT

STPS40L15CWPbF

Vishay High Power Products

Schottky Rectifier, 2 x 20 A



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PRODUCT SUMMARY				
I _{F(AV)} 2 x 20 A				
V _R	15 V			
I _{RM}	600 mA at 100 °C			

FEATURES

- 125 °C T_J operation ($V_R < 5 V$)
- · Center tap module
- · Optimized for OR-ing applications
- · Ultra low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for industrial level

DESCRIPTION

The STPS40L15CWPbF center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	40	А		
V _{RRM}		15	V		
I _{FSM}	t _p = 5 μs sine	700	А		
V _F	19 Apk, $T_J = 125 \ ^{\circ}C$ (per leg, typical)	0.25	V		
TJ		- 55 to 125	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	STPS40L15CWPbF	UNITS
Maximum DC reverse voltage	V _R	T.I = 100 °C	15	V
Maximum working peak reverse voltage	V _{RWM}	1j=100°C	15	v

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	L TEST CONDITIONS VALUES UN		UNITS		
Maximum average forward current per leg		per leg	50 % duty cycle at $T_{\rm C}$ = 86 °C, rectangular waveform		20	
See fig. 5 per device	IF(AV)	(AV) 50 % duty cycle at $\Gamma_{\rm C} = 80$ °C, rectangular wavelonn 40		40		
Maximum peak one cycle non-repetitive surge current per leg		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	700	A	
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse		330		
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 2 \text{ A}, L = 5 \text{ mH}$ 1		10	mJ	
Repetitive avalanche current per leg	I _{AR}	$I_{AR} \begin{array}{c} \mbox{Current decaying linearly to zero in 1 } \mu s \\ \mbox{Frequency limited by } T_{J} \mbox{ maximum } V_{A} = 1.5 \mbox{ x } V_{R} \mbox{ typical} \end{array} \right. 2$		2	А	

* Pb containing terminations are not RoHS compliant, exemptions may apply

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	19 A	• T _J = 25 °C	-	0.41	v
		40 A		-	0.52	
		19 A	T _J = 125 °C	0.25	0.33	
		40 A		0.37	0.50	
Reverse leakage current per leg	I _{BM} ⁽¹⁾	$T_J = 25 \ ^{\circ}C$	V _R = Rated V _R	-	10	mA
See fig. 2	IRM \''	$T_J = 100 \ ^{\circ}C$		-	600	ma
Threshold voltage	V _{F(TO)}	$T_J = T_J maximum$		0.1	82	V
Forward slope resistance	r _t			7	.6	mΩ
Maximum junction capacitance per leg	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		-	2000	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8		-	nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000			V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature range	TJ		- 55 to 125	°C	
Maximum storage temperature range	T _{Stg}		- 55 to 150	C	
Maximum thermal resistance, junction to case per leg		DC operation See fig. 4	1.4		
Maximum thermal resistance, junction to case per package	– R _{thJC}	DC operation	0.7	°C/W	
Typical thermal resistance, case to heatsink	R _{thCS}	hCS Mounting surface, smooth and greased			
			6	g	
Approximate weight			0.21	oz.	
Mounting torque minimum maximum		Non lubricated threads	6 (5)	kgf ⋅ cm	
		Non-lubricated threads	12 (10)	(lbf ⋅ in)	
Marking device		Case style TO-247AC (JEDEC) STPS4		L15CW	



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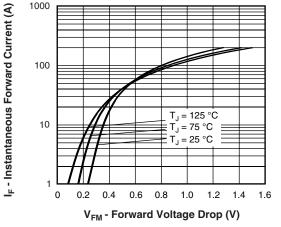


Fig. 1 - Maximum Forward Voltage Drop Characteristics

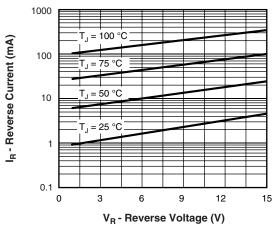


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

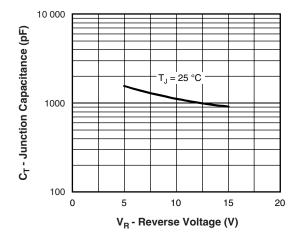


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

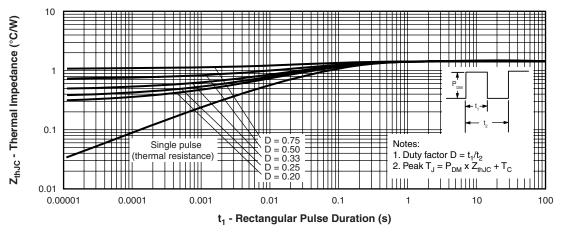
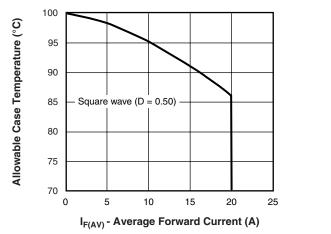


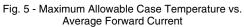
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

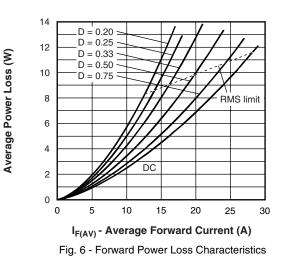
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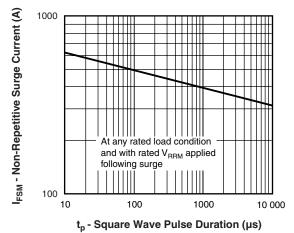


Fig. 7 - Maximum Non-Repetitive Surge Current

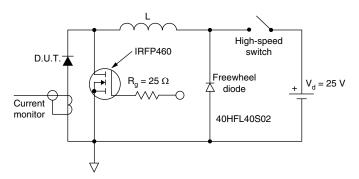
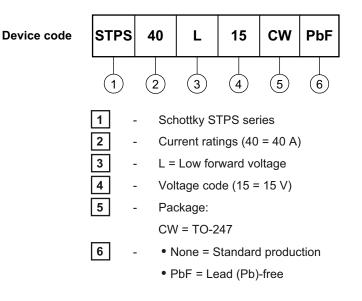


Fig. 8 - Unclamped Inductive Test Circuit



Schottky Rectifier, 2 x 20 A Vishay High Power Products

ORDERING INFORMATION TABLE



Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95223			
Part marking information	http://www.vishay.com/doc?95226		



Vishay

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